



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Keiichiro KATA et al.
Title: PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE
AND SEMICONDUCTOR WAFER
Application No.: 10/645,782
Filing Date: 8/22/2003
Examiner: Mark Prenty
Art Unit: 2822

AMENDMENT AND REPLY UNDER 37 C.F.R. 1.173

Mail Stop Amendment
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This amendment and reply is being filed in response to an Office Action dated January 19, 2006, for the above-identified application.

A petition and fee for a one-month extension of time is being submitted concurrently herewith.

Amendments to the Claims begin on page 2 of this document.

Remarks/Arguments begin on page 7 of this document.

Please amend the application as follows:

05/01/2006 JADD01 00000021 10645782

01 FC:1201

400.00 OP

02 FC:1251

120.00 OP